



501.43228X00

*JPW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. MURATA et al.

Serial No.: 10/756,419

Filed: January 14, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND A METHOD OF MANUFACTURING THE SAME

Group: 2814

Examiner: LE, Thao X.

**RESPONSE TO ELECTION REQUIREMENT**

Commissioner for Patents  
POB 1450  
Alexandria, VA 22313-1450

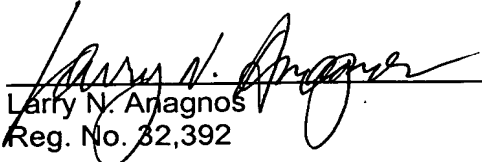
June 20, 2005

Sir:

In response to the Restriction Requirement, dated May 19, 2005,  
Applicants hereby elect Group I (including claims 1-14, 38-40 and 41), which  
are drawn to a semiconductor integrated circuit device, without traverse.  
Examination therefor of the same is respectfully requested.

To the extent necessary, applicants petition for an extension of time  
under 37 CFR §1.136. Please charge any shortage in the fees due in  
connection with the filing of this paper, to the Deposit Account of Antonelli,  
Terry, Stout & Kraus, LLP, Dep. Acct. No. 01-2135 (501.43228X00), and  
please credit any excess fees to such deposit account.

Respectfully submitted,  
ANTONELLI, TERRY, STOUT & KRAUS, LLP

  
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Larry N. Anagnos  
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